

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S26	614	(board or substrate or pwb or pcb) with (vdd) with (gnd or ground)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 18:59
S27	126	S26 and (high adj voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 18:59
S28	125	S27 not (S24 or S17 or S15 or S13 or S12 or S11 or S10 or S9 or S8 or S7 or S4 or S3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 19:01
S29	475	S26 not (S28 or S24 or S17 or S15 or S13 or S12 or S11 or S10 or S9 or S8 or S7 or S4 or S3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 19:06
S30	21	(vdd adj wiring) with (gnd adj wiring)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 19:11
S33	4	(pwb or pcb or wiring adj board or circuit adj board) with (split adj plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 14:35
S34	6	(pwb or pcb or wiring adj board or circuit adj board) same (split adj plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:58
S35	11	(pwb or pcb or wiring adj board or circuit adj board) and (split adj plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 12:07

S36	5	S35 not (S34 or S33)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:56
S37	19	(pwb or pcb or wiring adj board or circuit adj board) with (split with plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:56
S38	14	S37 not (S35 or S34 or S33)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:59
S39	26	(pwb or pcb or wiring adj board or circuit adj board) same (split with plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:59
S40	5	S39 not (S37 or S35 or S34 or S33)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 12:07
S41	109	(pwb or pcb or wiring adj board or circuit adj board) and (split with plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 12:07
S42	79	S41 not (S39 or S37 or S35 or S34 or S33)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 12:07
S46	7	(pwb or pcb or wiring adj board or circuit adj board) with (power adj distribution adj plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 14:45

S47	6	(pwb or pcb or wiring adj board or circuit adj board) and (trace\$1 or pattern\$1 or strip\$1) and (residual adj area\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 14:46
S48	11	(pwb or pcb or wiring adj board or circuit adj board) and (trace\$1 or pattern\$1 or strip\$1) and (residual adj area\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 14:47
S61	7680	(174/250-268).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/06/08 17:33
S62	1353	(361/792-795).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/06/08 17:33
S63	8211	S61 or S62	US-PGPUB; USPAT	OR	OFF	2005/06/08 17:34
S64	399	S63 and (power adj plane)	US-PGPUB; USPAT	OR	OFF	2005/06/08 17:34